

Short term training program (STTP)

on

“Real-time Signal Processing using FPGA”

11th to 16th June 2018

Organized By: Department of Electronics & Electrical Engg.

REGISTRATION FORM

Name :

Designation :

Qualification :

Institution :

Address :

Mobile no. :

Email id :

Gender Male Female

Accommodation required Yes No

(On Payment Basis)

(Limited on campus hostel accommodation 2-3 persons sharing is available)

Registration Category Faculty Research Scholars Student Industry Participant

Registration Fee Details

Amount:

Mode of Payment:

Date:

DD No. / Reference No./serial No.:

Bank & Branch:

Date:

Signature

Note: Send the duly filled SCAN copy on aniket.kumar@shobhituiversity.ac.in